

WEST Search History

DATE: Wednesday, December 27, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L17	l15 and L16	1
<input type="checkbox"/>	L16	((("design-specific" or "design specific") near3 cell))[clm]	4
<input type="checkbox"/>	L15	((transistorlevel or "transistor-level" or "transistor level") near2 (netlist or "net-list" or "net list"))[clm]	17
<input type="checkbox"/>	L14	(generat\$ with (("design-specific" or "design specific") near3 cell))[clm]	3
<input type="checkbox"/>	L13	l10 and L12	6
<input type="checkbox"/>	L12	(partition\$ with (block or cell))[clm]	1774
<input type="checkbox"/>	L11	L10 and (design near2 objective)[clm]	1
<input type="checkbox"/>	L10	(describ\$ with ("integrated circuit" or ic))[clm]	106
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L9	((transistorlevel or "transistor-level" or "transistor level") near2 (netlist or "net-list" or "net list")) same (design near2 specification)	3
<input type="checkbox"/>	L8	l2 and L7	8
<input type="checkbox"/>	L7	("design-specific" or "design specific")	5506
<input type="checkbox"/>	L6	l4 or l5	58
<input type="checkbox"/>	L5	L2 and partition\$	58
<input type="checkbox"/>	L4	L3 and partition\$	30
<input type="checkbox"/>	L3	l1 and L2	99
<input type="checkbox"/>	L2	(transistorlevel or "transistor-level" or "transistor level") near2 (netlist or "net-list" or "net list")	277
<input type="checkbox"/>	L1	716/\$(ccls]	11355

END OF SEARCH HISTORY